5 V Low-Drop Voltage Regulator

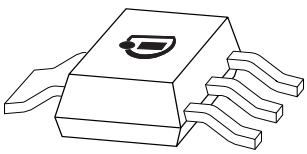
TLE 4266-2

Target Data

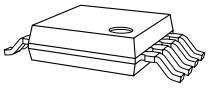
Bipolar IC

Features

- Output voltage tolerance ≤ ± 2 %
- 150 mA current capability
- Very low current consumption
- Low-drop voltage
- Overtemperature protection
- Reverse polarity proof
- Wide temperature range
- Suitable for use in automotive electronics
- Inhibit



SOT-223



P-TSOP-8-1

Type	Ordering Code	Package
TLE 4266-2 G	Q67006-A9485	P-SOT223-4-2
▼ TLE 4266-2 GS	Q67006-A9486	P-TSOP-8-1

▼ New type

Functional Description

The TLE 4266-2 is a monolithic integrated low-drop fixed voltage regulator which can supply loads up to 150 mA. It can be switched on and off by the $\overline{\text{INH}}$ pin. It is functional compatible to the TLE 4266, but with a reduced quiescent current of <<1 µA in OFF mode and 35 µA in ON mode. The TLE 4266-2 is especially designed for all applications that require very low quiescent current in ON and OFF mode. The device is available in the small surface mounted P-SOT223-4-2 and Micro-8 P-TSOP-8-1 package. In the P-SOT223-4-2 housing it is pin compatible to the TLE 4266G. It is designed to supply microprocessor systems under the severe condition of automotive applications and therefore it is equipped with additional protection against over load, short circuit and overtemperature. Of course the TLE 4266-2 can be used in other applications, where a stabilized voltage and the inhibit feature is required.

And input voltage V_I in the range of 5.5 V < V_I < 45 V is regulated to V_Q = 5 V with an accuracy of ± 2%.

The device operates in the temperature range of T_j = – 40 to 150 °C. A High level at the $\overline{\text{INH}}$ pin switches the regulator on.

P-SOT223-4-2

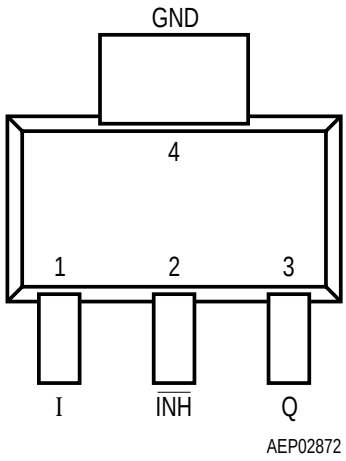


Figure 1 **Pin Configuration** (top view)

Pin Definitions and Functions TLE 4266-2 G

Pin	Symbol	Function
1	I	**Input voltage;** block to ground directly at the IC with a ceramic capacitor.
2	$\overline{\text{INH}}$	**Inhibit input;** high level turns IC on.
3	Q	**Output voltage;** block to ground with a capacitor. $C \geq 10\ \mu F$, ESR < 5 Ω
4	GND	**Ground**

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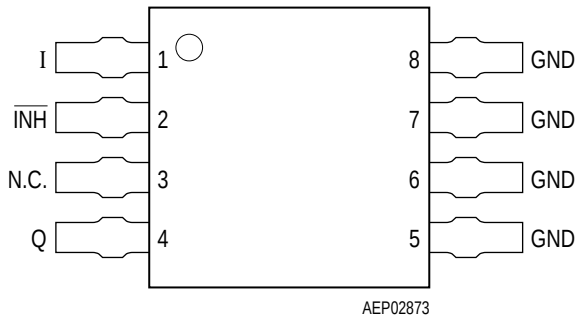


Figure 2 **Pin Configuration** (top view) (cont'd)

Pin Definitions and Functions TLE 4266-2 GS

Pin	Symbol	Function
1	I	**Input voltage;** block to ground directly on IC with a ceramic capacitor
2	$\overline{\text{INH}}$	**Inhibit input;** high level turns IC on.
3	N.C.	Not Connected
4	Q	**Output voltage;** block to ground with a capacitor. $C \geq 10\ \mu F$, ESR < 5 Ω
5, 6, 7, 8	GND	**Ground**

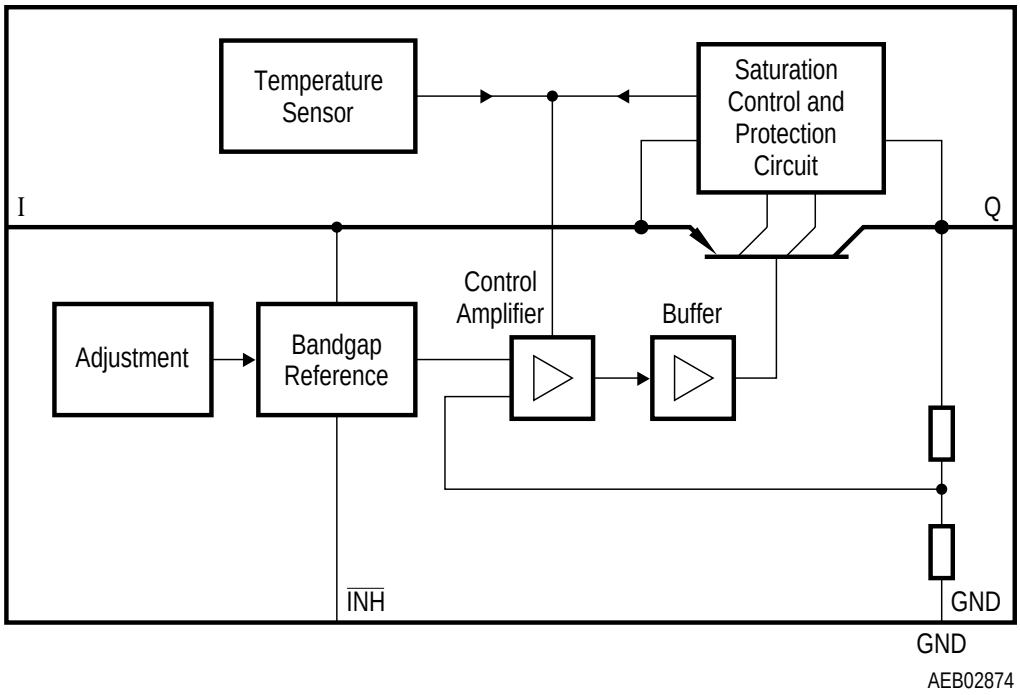


Figure 3 Block Diagram

Absolute Maximum Ratings

T_j = – 40 to 150 °C

Parameter	Symbol	Limit Values		Unit	Notes
		min.	max.		
Input I					
Voltage	V_I	– 42	45	V	–
Current	I_I	–	–	–	internally limited
Inhibit $\overline{\text{INH}}$					
Voltage	$V_{\overline{\text{INH}}}$	– 42	45	V	–
Output Q					
Voltage	V_Q	– 1	32	V	–
Current	I_Q	–	–	–	internally limited
GND					
Current	I_{GND}	50	–	mA	–
Temperature					
Junction temperature	T_j	–	150	°C	–
Storage temperature	T_S	– 50	150	°C	–
Operating Range					
Input voltage	V_i	5.5	45	V	–
Junction temperature	T_j	– 40	150	°C	–
Thermal Resistance					
Junction ambient	$R_{thj\text{-}a}$	–	85	K/W	P-SOT223-4-2[1)]
Junction ambient	$R_{thj\text{-}a}$	–	115	K/W	P-TSOP-8-1[1)]
Junction case	$R_{thj\text{-}pin4}$	–	20	K/W	P-SOT223-4-2
Junction case	$R_{thj\text{-}pin5\text{-}8}$	–	t.b.d.	K/W	P-TSOP-8-1

[1)] Worst case, regarding peak temperature; zero airflow; mounted an a PCB 80 × 80 × 1.5 mm³, heat sink area 300 mm².

Characteristics

V_I = 13.5 V; – 40 °C ≤ T_j ≤ 125 °C

Parameter	Symbol	Limit Values			Unit	Test Condition
		min.	typ.	max.		
Output voltage	V_Q	4.9	5	5.1	V	5 mA ≤ I_Q ≤ 100 mA 6 V ≤ V_i ≤ 28 V
Output-current limitation	I_Q	150	200	500	mA	–
Current consumption $I_q = I_i - I_Q$	I_q	–	0	1	µA	$V_{\overline{INH}}$ = 0 V; T_j ≤ 100 °C
Current consumption $I_q = I_i - I_Q$	I_q	–	35	–	µA	I_Q = 1 mA Inhibit ON
Current consumption $I_q = I_i - I_Q$	I_q	–	2	8	mA	I_Q = 50 mA Inhibit ON
Drop voltage	V_{Dr}	–	0.25	0.5	V	I_Q = 100 mA[1)]
Load regulation	ΔV_Q	–	10	30	mV	I_Q = 1 to 100 mA V_i = 6 V
Line regulation	ΔV_Q	–	10	40	mV	V_I = 6 V to 28 V I_Q = 1 mA
Supply-voltage rejection	SVR	–	68	–	dB	f_r = 100 Hz, V_r = 0.5 V_{PP}
Inhibit						
Inhibit on voltage	$V_{\overline{INH},\,on}$	3.5	–	–	V	–
Inhibit off voltage	$V_{\overline{INH},\,off}$	–	–	1	V	–
Inhibit current	$I_{\overline{INH}}$	–	4	8	µA	$V_{\overline{INH}}$ = 5 V

[1)] Drop voltage = $V_i - V_Q$ (measured when the output voltage V_Q has dropped 100 mV from the nominal value obtained at V_i = 13.5 V).

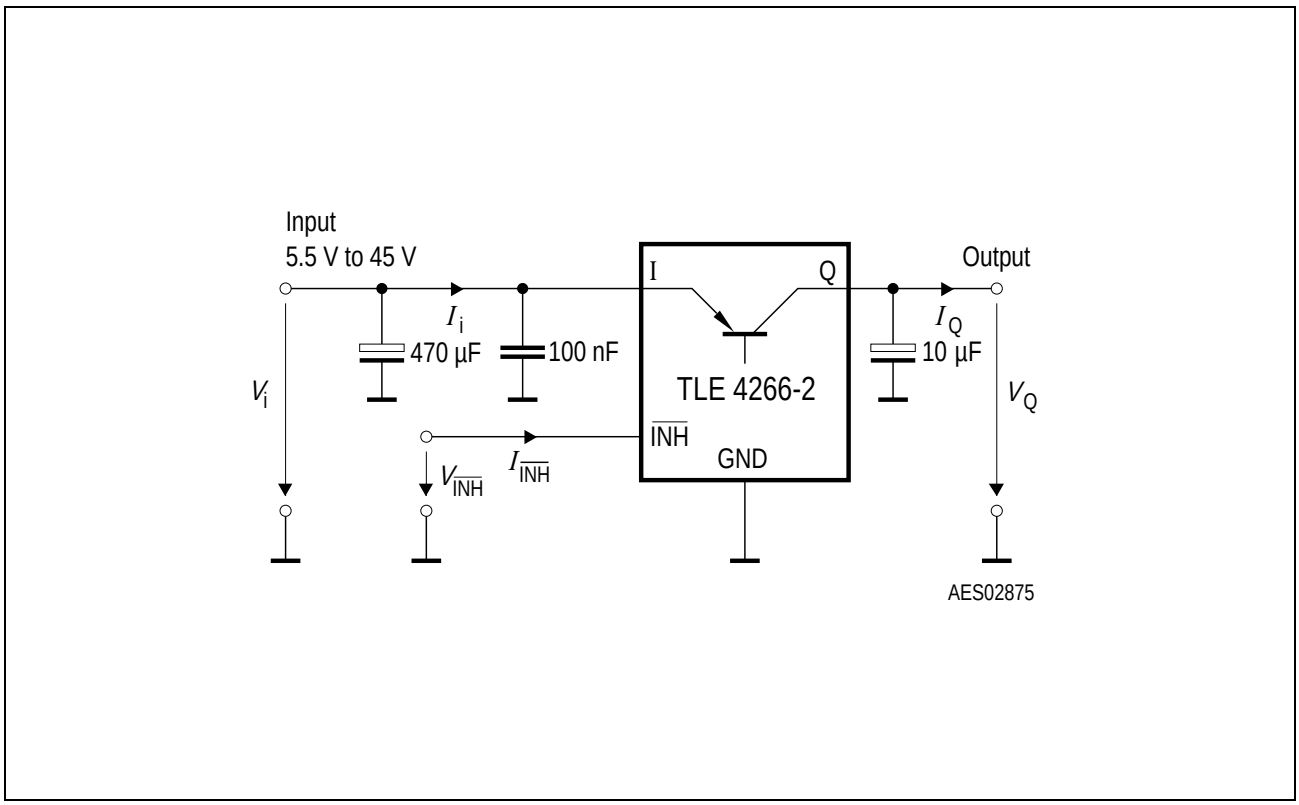


Figure 4 Measuring Circuit

Application Information

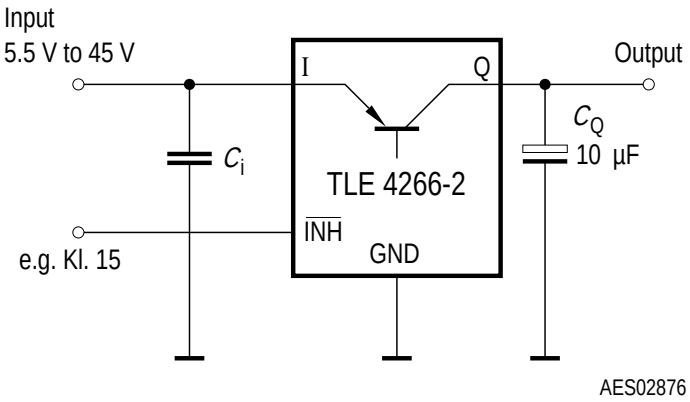


Figure 5 Application Circuit

In the TLE 4266-2 the output voltage is divided and compared to an internal reference of 2.5 V typical. The regulation loop controls the output voltage to achieve an output voltage of 5 V with an accuracy of ± 2% at an input voltage range of 5.5 V < V_I < 45 V.

Output

For stability of the control loop the TLE 4266-2 output requires an output capacitor of at least 10 µF with an ESR below 5 Ω.

The TLE 4266-2 can supply 150 mA. However for protection for high input voltages above 25 V, the output current is reduced (SOA protection).

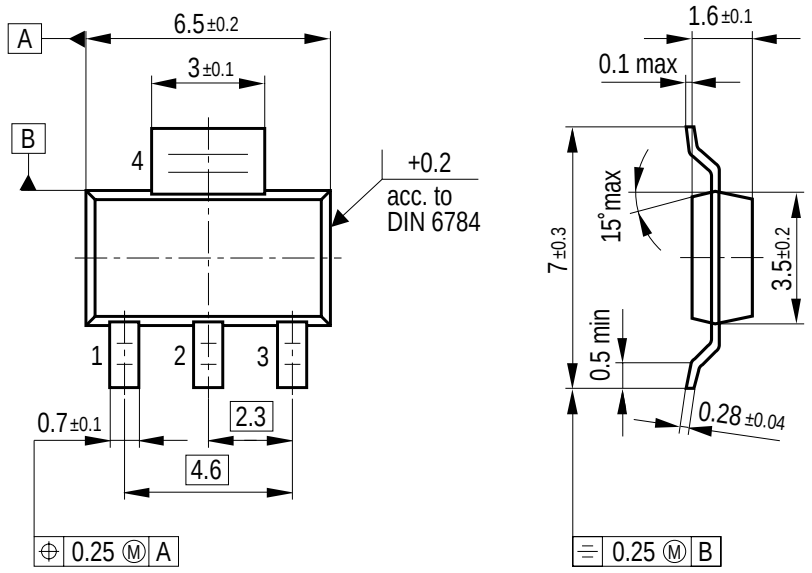
At the input of the regulator an input capacitor is necessary for compensating line influences. A resistor of approx. 1 Ω in series with C_I, can damp the LC of the input line inductivity and the input capacitor.

Inhibit Function

The TLE 4266-2 includes the Inhibit function. For a voltage above 3.5 V at the $\overline{\text{INH}}$ pin the regulator is switched on.

Package Outlines

P-SOT223-4-2
(Plastic Small Outline Transistor)



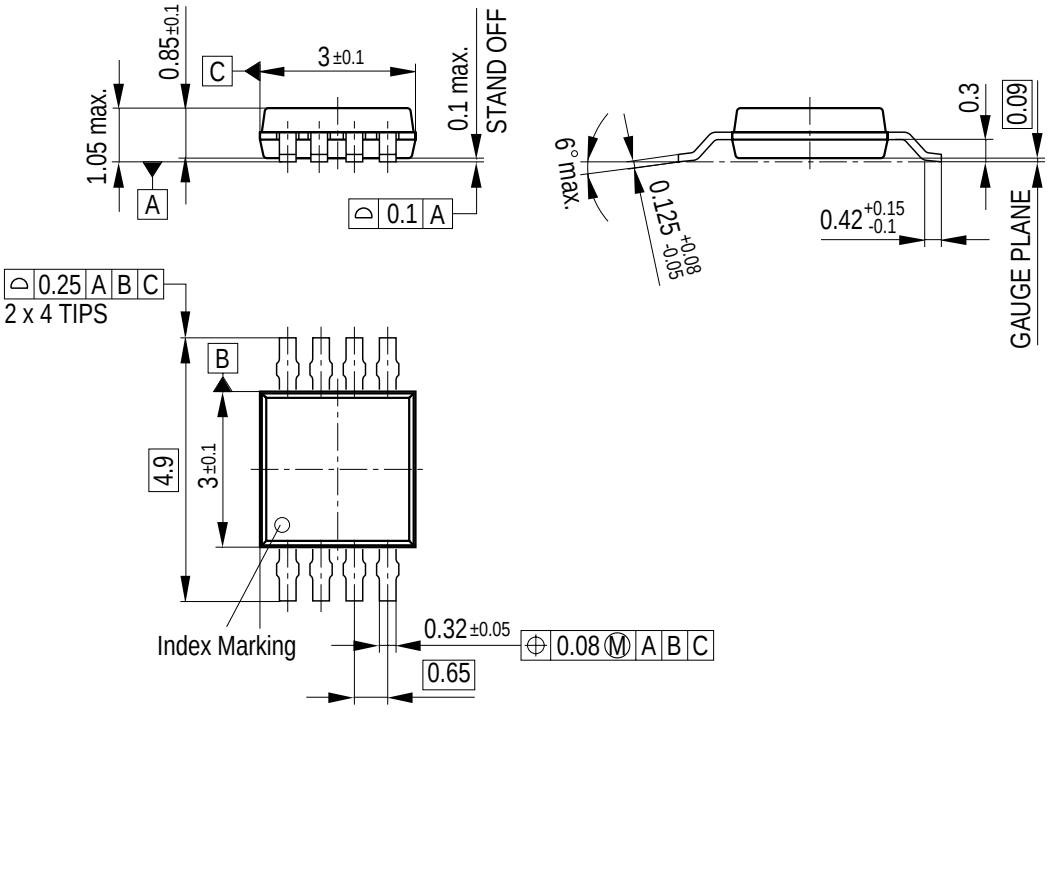
Weight approx. 0.15 g

Sorts of Packing
Package outlines for tubes, trays etc. are contained in our Data Book “Package Information”

SMD = Surface Mounted Device

Dimensions in mm

P-TSOP-8-1
(Plastic Thin Small Outline Package)



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Dimensions in mm

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